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PATENT

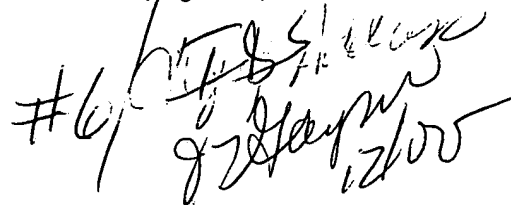
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Peter Warnes, et al.
App. No. : 09/523,877
Filed : March 13, 2000
For : **APPARATUS AND METHOD
FOR JUMP DELAY SLOT
CONTROL IN A PIPELINED
PROCESSOR**
Examiner : Unknown
Group Art : 2784

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on

December 13, 2000
(Date)


Robert F. Gazdzinski, Reg. No. 39,990

#6 

TRANSMITTAL LETTER

Assistant Commissioner for Patents
Washington, D.C. 20231

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Dear Sir:

Enclosed for filing in the above-identified application are:

1. Information Disclosure Statement (2 pages);
2. Form PTO-1449 (1 page) enclosing four (4) references;
3. Return prepaid postcard.

Respectfully submitted,
GAZDZINSKI & ASSOCIATES

Dated: December 13, 2000

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PATENT

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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Enclosed is form PTO-1449 listing references that are also enclosed. Pursuant to 37 C.F.R. 1.97(b)(3) and 37 C.F.R. § 1.97(e)(1), the information contained in the attached PTO-1449 form was cited in a communication from a foreign patent office in a counterpart foreign application not more than three (3) months prior to the filing of this statement.

Pursuant to 37 C.F.R. § 1.97(g) and (h), Applicant makes no representation that a search has been made or that the information cited is considered to be material to patentability. Additionally, inclusion on this list is not an admission that any of the cited documents are prior

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art in this application. Further, Applicant makes no representation regarding the completeness of this list or that better art does not exist.

Respectfully submitted,

GAZDZINSKI & ASSOCIATES

Dated: December 13, 2000

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